

Notice of References Cited	Application/Control No. 09/893,217	Applicant(s)/Patent Under Reexamination LYSDAL ET AL.	
	Examiner Eva Yi Zheng	Art Unit 2634	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,725,390	04-2004	Liu et al.	713/401
	B	US-5,526,359	06-1996	Read et al.	370/516
	C	US-6,219,395	04-2001	Pollack et al.	375/371
	D	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Drerup et al.; Dynamic Phase Alignment Circuit; Pub. No.: US 2002/0071510 A1; Pub. Date: Jun 13, 2002
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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